SN: 10/645,861 MST-012-1P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Wingyu Leung et al.

Assignee: Monolithic System Technology, Inc.

Title: Transparent Error Correcting Memory

Serial No: 10/645,861 Filing Date: 08/20/2003

Examiner: Stephen M. Baker Art Unit: 2133

Attorney Docket No: MST-012-1P

Date: September 20, 2007

Mail Stop Amendment COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO SECOND OFFICE ACTION

Sir:

Applicants submit the following remarks and amendments in response to the Office Action dated March 20, 2007. A petition for a 3-month extension, extending the period for response to September 20, 2007, accompanies the present response.